



PRODUCT SPECIFICATIONS

For Customer: _____ : APPROVAL FOR SPECIFICATION

Customer Model No. _____ : APPROVAL FOR SAMPLE

Module No.: RXL070118-A

Date : 2015.05.08

Version :0

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For Customer's Acceptance:

Approved By	Comment

PREPARED	CHECKED	VERIFIED BY QA DEPT	VERIFIED BY R&D DEPT



2. Revision Record

Date	Rev.N o.	Page	Revision Items	Prepared
2015.05.08	V0		The first release	



3. General Specifications

RXL070118-A is a color active matrix TFT LCD single cell using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This panel has a 7 inch diagonally measured active area with WSVGA resolutions (1024 horizontal by 600 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors.

The followings are general specifications at the single cell.

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	154.2144(H) × 85.92(V)	mm	
CF size	159(H) × 91(V)	mm	
Number of pixels	1024 (H) × 600 (V)	pixels	
Pixel pitch	0.0502(H) × 0.143.2(V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	16.7M	colors	
Color Gamut	50	%	
Display mode	Normally White		
Panel Size	162.2 (H) x 95.7 (V)	mm	



4.Outline.Drawing

1	2	3	4	5	6
客户 For Customer:		客户料号:	客户版本:	描述/修订内容	REV/修订日期
		承认日期:	承认签名:		

ROHS

SM0701BET302-3TE501 YYYY/MM/DD

Notes:

- LED CIRCUIT DIAGRAM:
- RoHS must be complied.
- Modification rev.number
- Drafit angle 1.5°
- All radii without dimension R0.3. Unspecified Tolerances is: ±0.2

Electrical-Optical Characteristics (Ta=25°C):

Item (项目)	Symbol(符号)	min.(最小)	typ.(典型)	max(最大)	Unit (单位)	Condition (测试条件)
Luminance (亮度)	Lv		250		cd/m ²	
Uniformity (均匀度)	Avg	75			%	
Colour (色度坐标)	X	0.26	0.315			If= 140 mA(恒定电流测试)
Coordinate	Y	0.27	0.325			
Forward Voltage (正向电压)	Vf	9	10.5		V	
Reverse Current (反向电流)	Ir	20			mA	Vr= 0.8V
Operating Temperature (工作温度)		-0~+50°C			Storage Temperature (贮存温度)	-20~+60°C
Storage condition Recommended:		temperature (25°C±5°C) and humidity (65%RH±20%RH)				

MODEL NO.: RXL070118-A		
模组型号:	THE THIRD ANGLE PROJECTION (第三角法)	EDITION 版本号: A/0
UNIT (单位) mm	DESIGN 设计:	DATE 日期:
CHECKED 审核:	APPROVED 批准:	DATE 日期:



5. Absolute Maximum Ratings(Ta=25°C)

Environmental Absolute Maximum Ratings

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30°C	80°C	-20°C	70°C	1,2
Humidity	-	-	-	-	3

1. The response time will become lower when operated at low temperature.
2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

3. Ta<=40°C:85%RH MAX.

Ta>=40°C:Absolute humidity must be lower than the humidity of 85%RH at 40°C.

6. Electrical Specifications and Instruction Code

6.1 Electrical Characteristics(Vss=0V,Ta=25°C)

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Digital Power Supply Voltage For LCD	DVDD	1.8	2.0		V	
Analog Power Supply Voltage	AVDD	9.4	9.6	9.8	V	
Gate On Power Supply Voltage	VGH	17	18	19	V	
Gate Off Power Supply Voltage	VGL	-6.6	-6	-5.4	V	
Common Power Supply Voltage	VCOM	3.6	3.8	4.0	V	Note 1
Logic Input Voltage	VIH	0.7*DVDD		DVDD	V	
	VIL	GND		0.3*DVDD	V	

Note1: Please adjust VCOM to make the flicker level be minimum.



6.2 LED backlight specification(VSS=0V ,Ta=25°C)

Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Supply voltage	-	-	-	9.6	-	V	1
Supply current	I _f	-	-	140	-	mA	2
Forward current	Normal	I _{pn}	3-chip series x 7	-	140	-	mA
	Dimming	I _{pd}		-	-	-	

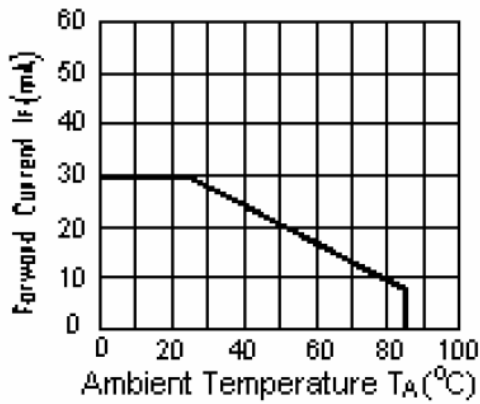
Note:

1: V_{LED}=V_{LED}(+)-V_{LED}(-).

2:The current of LED is 20mA.

A LED drive in constant current mode is recommended.

3: LED power consumption is around 0.297W.



CIRCUIT DIAGRAM

I_{LED} VS TEMP

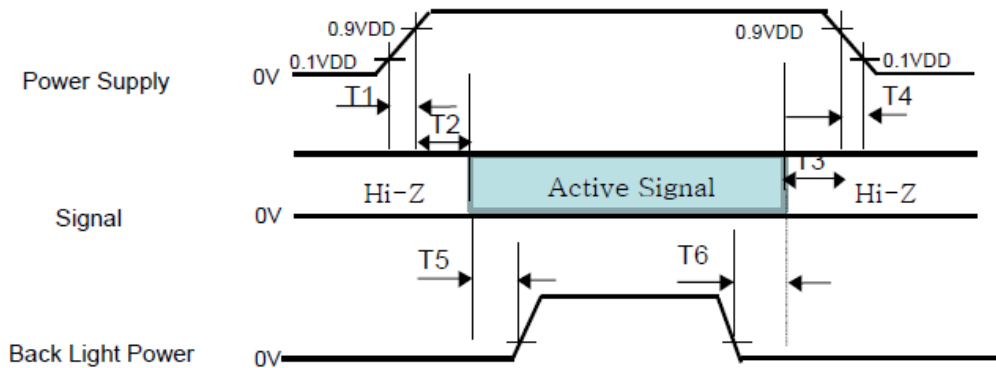


6.3 Interface signals

Pin No.	Symbol	Function	Remark
1-2	LED+	LED Anode	
3	VGH	Gate ON Voltage	
4	VGL	Gate OFF Voltage	
5	UPDN	Gate Up or Down scan control	
6	SHLR	Source Right or Left sequence	
7-8	LED-	LED Cathode	
9	AVDD	Power for Analog Circuit	
10	GND	Ground	
11	RXIN3P	MIPI Data lane3 input	
12	RXIN3N		
13	GND	Ground	
14	RXIN2P	MIPI Data lane2 input	
15	RXIN2N		
16	GND	Ground	
17	RXCLKP	MIPI CLK input	
18	RXCLKN		
19	GND	Ground	
20	RXIN1P	MIPI Data lane1 input	
21	RXIN1N		
22	GND	Ground	
23	RXIN0P	MIPI Data lane0 input	
24	RXIN0N		
25	GND	Ground	
26	STBYB	Standby mode	
27	RESET	Global reset pin	
28	VDD	Power Supply (1.8V)	
29	VDD	Power Supply (1.8V)	
30	VCOM	Common Voltage	



6.4 Power On/Off Sequence



- $0.5\text{ms} \leq T1 \leq 10\text{ms}$
- $0\text{ms} \leq T2$
- $0\text{ms} \leq T3$
- $0\text{ms} \leq T4 \leq 10\text{ms}$
- $100\text{ms} \leq T5$
- $0\text{ms} \leq T6 \leq 10\text{ms}$

Notes:

1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
2. Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid.



6.5 OPTICAL SPECIFICATION

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25 \pm 2^\circ\text{C}$) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and ϕ equal to 0° . We refer to $\theta=0$ ($=\theta_3$) as the 3 o'clock direction (the "right"), $\theta=90$ ($=\theta_{12}$) as the 12 o'clock direction ("upward"), $\theta=180$ ($=\theta_9$) as the 9 o'clock direction ("left") and $\theta=270$ ($=\theta_6$) as the 6 o'clock direction ("bottom"). While scanning θ and/or ϕ , the center of the measuring spot on the Display surface shall stay fixed. Optimum viewing angle direction is 12 o'clock.

<Table 5. Optical Specifications>

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle range	Horizontal	θ_3	CR > 10	-	85	-	Deg.	Note 1
		θ_9		-	85	-	Deg.	
	Vertical	θ_{12}		-	85	-	Deg.	
		θ_6		-	85	-	Deg.	
Color Gamut (C light)			-	50	-	%		
Luminance Contrast ratio		CR	$\theta = 0^\circ$	-	800			Note 2
Transmittance		T(%)	$\theta = 0^\circ$	-	5.0	-	%	Base on C Light Note 3
White Chromaticity		x_w	$\theta = 0^\circ$	0.273	0.303	0.333		Note 4 C light
		y_w		0.303	0.333	0.363		
Reproduction of color (C light)	Red	x_R	$\theta = 0^\circ$	0.588	0.618	0.648		
		y_R		0.296	0.326	0.356		
	Green	x_G		0.255	0.285	0.315		
		y_G		0.509	0.539	0.569		
	Blue	x_B		0.116	0.146	0.176		
		y_B		0.118	0.148	0.178		
Response Time (Rising + Falling)		T_{RT}	Ta= 25°C $\theta = 0^\circ$	-	25	40	ms	Note 5



Note :

1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 5).
2. Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (see FIGURE 5) Luminance Contrast Ratio (CR) is defined mathematically.

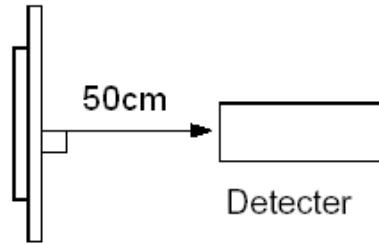
$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. Transmittance is the Value with Polarizer.
4. The color chromaticity coordinates specified in Table 6 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
5. The electro-optical response time measurements shall be made as FIGURE 6 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_d .

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: $T_a=25^{\circ}\text{C}$.
- Adjust operating voltage to get optimum contrast at the center of the display.

Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

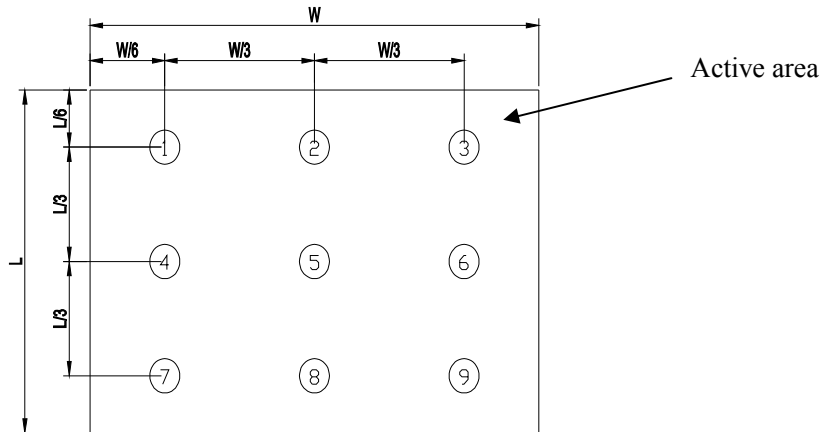


Note 2: The luminance uniformity is calculated by using following formula.

$$\Delta B_p = B_p (\text{Min.}) / B_p (\text{Max.}) \times 100 (\%)$$

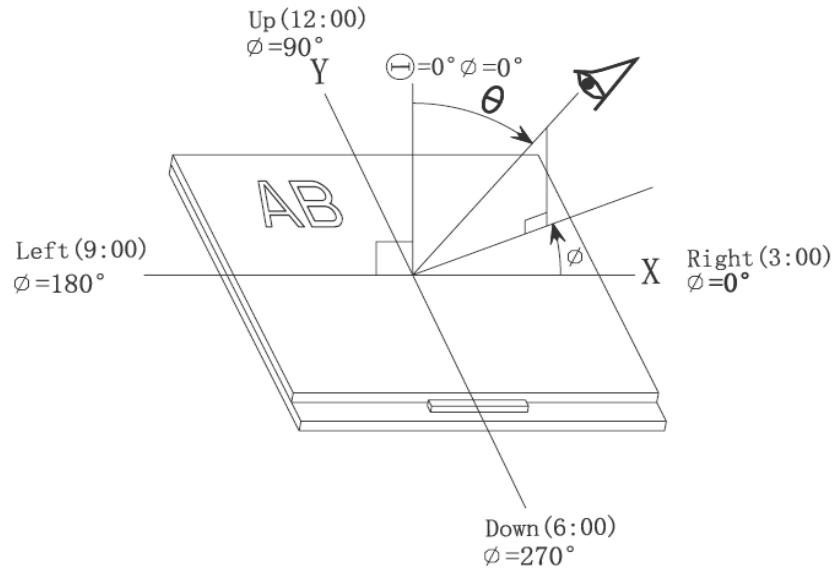
$B_p (\text{Max.})$ = Maximum brightness in 9 measured spots

$B_p (\text{Min.})$ = Minimum brightness in 9 measured spots.

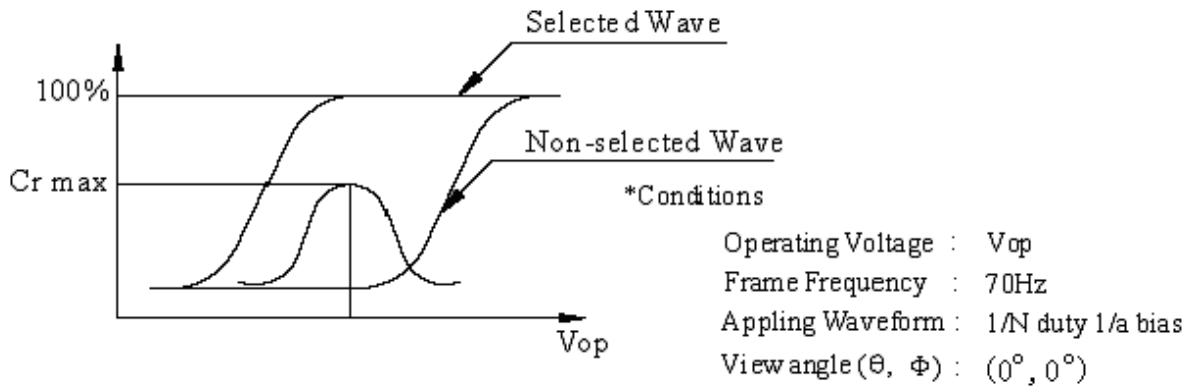


Note 3: The definition of viewing angle:

Refer to the graph below marked by θ and ϕ



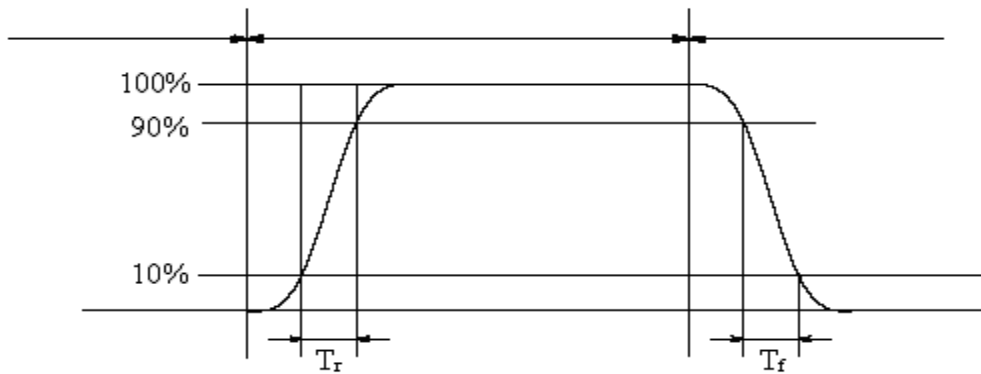
Note 4: Definition of contrast ratio.(Test LCD using DMS501)



$$\text{Contrast ratio}(Cr) = \frac{\text{Brightness of selected dots}}{\text{Brightness of non-selected dots}}$$

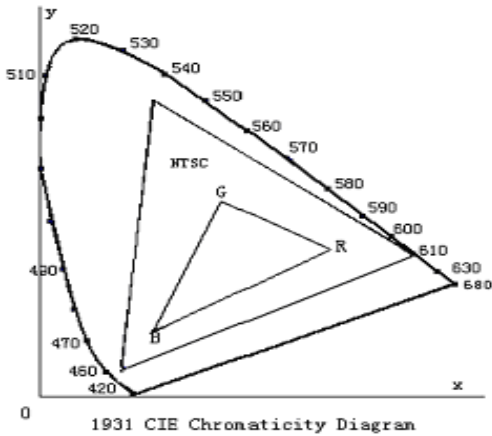
Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes.Refer to figure as below.



The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.

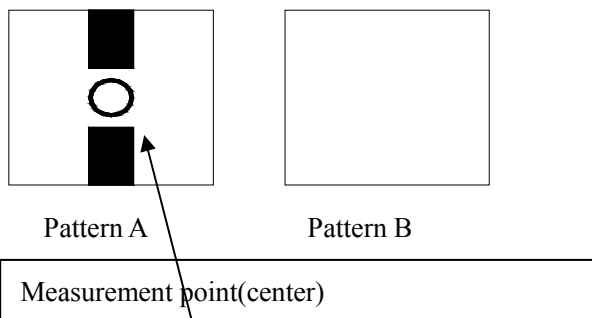


Color gamut:

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 7: Definition of cross talk.

$$\text{Cross talk ratio(\%)} = \frac{|\text{pattern A Brightness} - \text{pattern B Brightness}|}{\text{pattern A Brightness}} \times 100$$



Electric volume value=3F+/-3Hex



8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion
1	High Temperature Storage	80°C±2°C 96H Restore 2H at 25°C Power off	1. After testing, cosmetic and electrical defects should not happen. 2. Total current consumption should not be more than twice of initial value.
2	Low Temperature Storage	-30°C±2°C 96H Restore 2H at 25°C Power off	
3	High Temperature Operation	70°C±2°C 96H Restore 2H at 25°C Power on	
4	Low Temperature Operation	-20°C±2°C 96H Restore 4H at 25°C Power on	
5	High Temperature/Humidity Operation	60°C±2°C 90%RH 96H Power on	
6	Temperature Cycle	-30°C → 80°C 30min 5min 30min after 5 cycle, Restore 2H at 25°C Power off	
7	Vibration Test	10Hz~150Hz, 100m/s ² , 120min	Not allowed cosmetic and electrical defects.
8	Shock Test	Half- sine wave, 300m/s ² , 11ms	
9	ESD Test	Air discharge: +/-8KV, Contact discharge: 4KV	

Note: Operation: Supply 2.8V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection
Contrast	CR>50%
IDD	IDD<200%
Brightness	Brightness>60%
Color Tone	Color Tone+/-0,05

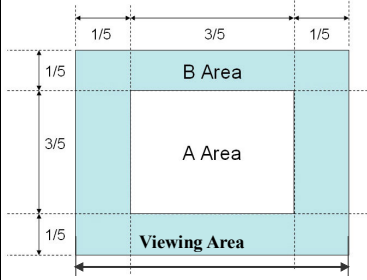
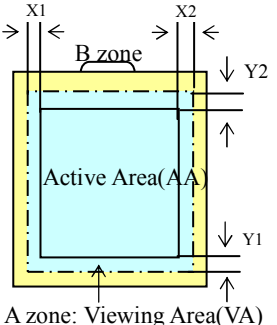
9 Quality level

9.1 Classification of defects

Major defects (MA): A major defect refers to a defect that may substantially degrade usability for product applications, including all functional defects (such as no display, abnormal display, open or missing segment, short circuit, missing component), outline dimension beyond the drawing, progressive defects and those affecting reliability.

Minor defects (MI): A minor defect refers to a defect which is not considered to be able to substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation, such as black spot, white spot, bright spot, pinhole, black line, white line, contrast variation, glass defect, polarizer defect, etc.

9.2 Definition of inspection range

<p>For dot defect of TFT LCD which is not smaller than 3 inches, dividing three areas to make a judgment (according to figure 1).</p> <p>A area : center of viewing area B area : periphery of viewing area C area : Outside viewing area</p> <p>For other defects, dividing two areas to make a judgment (according figure 2).</p> <p>A zone : Inside Viewing area B zone : Outside Viewing area</p> <p>X1(A.A~V.A): 2mm X2(A.A~V.A): 2mm Y1(A.A~V.A): 2mm Y2(A.A~V.A): 2mm</p>	 <p>Figure 1</p>  <p>Figure 2</p>
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9.3 Inspection items and general notes

General notes	<p>1. Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and TIANMA.</p> <p>2. Viewing area should be the area which TIANMA guarantees.</p> <p>3. Limit sample should be prior to this Inspection standard.</p> <p>4. Viewing judgment should be under static pattern.</p> <p>5. Inspection conditions Inspection distance: 250 mm (from the sample) Temperature : 25±5 °C Inspection angle : 45 degrees in 6 o'clock direction (all defects in viewing area should be inspected from this direction)</p>	
Inspection items	Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble	<p>The color of a small area is different from the remainder. The phenomenon doesn't change with voltage</p>
	Contrast variation	<p>The color of a small area is different from the remainder. The phenomenon changes with voltage</p>
	Polarizer defect	<p>Scratch, Dirt, Particle, Bubble on polarizer or between polarizer and glass</p>



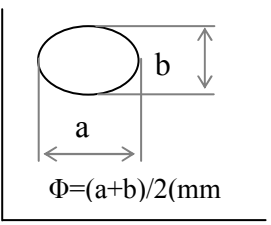
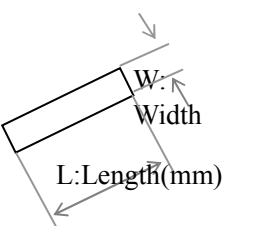
	Dot defect (TFT LCD)	The pixel appears bright or dark abnormally when display
	Functional defect	No display, Abnormal display, Open or missing segment, Short circuit, False viewing direction
	Glass defect	Glass crack, Shaved corner of glass, Surplus glass
	PCB defect	Components assembly defect

9.4 Outgoing Inspection level

Outgoing Inspection standard	Inspection conditions	Inspection				
		Min.	Max.	Unit	IL	AQL
Major Defects	See 8.3 general notes	See 8.5			II	0.065
Minor Defects	See 8.3 general notes	See 8.5			II	0.065

Note: Sampling standard conforms to GB2828

9.5 Inspection Items and Criteria

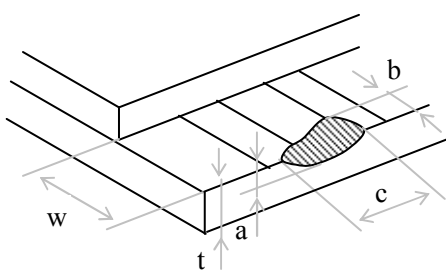
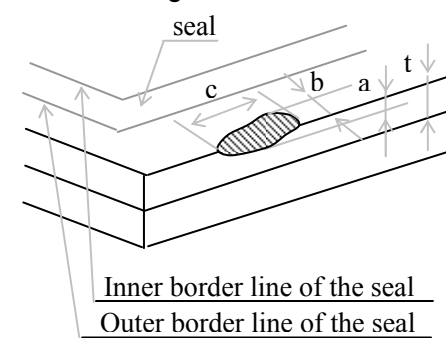
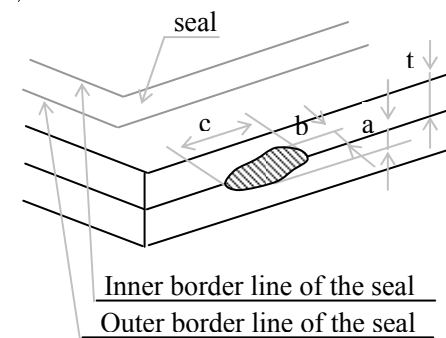
Inspection items		Judgment standard			
		Category	Acceptable number		
			A zone	B zone	
1	Black spot, White spot, Pinhole, Foreign Particle, Particle in or on glass, Scratch on glass  $\Phi=(a+b)/2(\text{mm})$ $(a/b<2.5)$	A	$\Phi \leq 0.20$	Neglected	Neglected
		B	$0.20 < \Phi \leq 0.25$	3	Neglected
		C	$0.25 < \Phi \leq 0.3$	2	Neglected
		D	$0.3 < \Phi \leq 0.4$	1	3
		E	$0.4 < \Phi \leq 0.5$	0	2
		Total defective point(B,C)		1	-
		2	Black line, White line, and Particle Between Polarizer and glass, Scratch on glass  $L/W \geq 2.5$	A	$W \leq 0.03$
B	$0.03 < W \leq 0.05$ $L \leq 3.0$			3	Neglected
C	$0.05 < W \leq 0.1$ $L \leq 3.0$			2	Neglected
D	$0.05 < W \leq 0.1$ $L \leq 4.0$			1	3
E	$W > 0.1$ $L > 4.0$			0	2

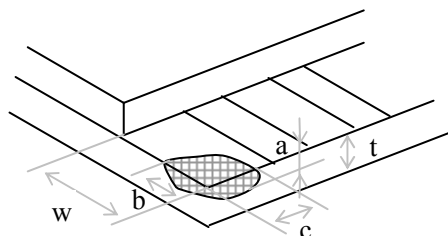


			Total defective point(B,C)	1	-	
3	Bright spot		any size	none	none	
4	Contrast variation		A	$\Phi < 0.2$	Neglected	
			B	$0.2 < \Phi \leq 0.3$	2	
			C	$0.3 < \Phi \leq 0.4$	1	
			D	$0.4 < \Phi$	0	
			Total defective point(B,C)		3	
5	Bubble inside cell		any size	none	none	
6	Polarizer defect (if Polarizer is used)	Scratch ,damage on polarizer, Particle on polarizer or between polarizer and glass.	Refer to item 1 and item 2.			
		Bubble, dent and convex	A	$\Phi \leq 0.1$	Neglected	Neglected
			B	$0.1 < \Phi \leq 0.2$	2	Neglected
			C	$0.2 < \Phi \leq 0.3$	1	2
7	Surplus glass	Stage surplus glass	 $B \leq 0.3\text{mm}$			
		Surrounding surplus glass	 Should not influence outline dimension and assembling.			
8	Open segment or open common		Not permitted			
9	Short circuit		Not permitted			
10	False viewing direction		Not permitted			
11	Contrast ratio uneven		According to the limit specimen			
12	Crosstalk		According to the limit specimen			
13	Black /White spot(display)		Refer to item 1			



14	Black /White line(display)	Refer to item 2
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Inspection items		Judgment standard		Acceptable number		
		Category(application: B zone)				
15	Glass defect crack	i) The front of lead terminals 	A	$a \leq t, b \leq 1/5W, c \leq 3\text{mm}$	Max.3 defects allowed	
			B	Crack at two sides of lead terminals should not cover patterns and alignment mark		
		ii) Surrounding crack-non-contact side 				$b < \text{Inner borderline of the seal}$
		iii) Surrounding crack- contact side 				$b < \text{Outer borderline of the seal}$
		iv) Corner	A	$a \leq t, b \leq 3.0, c \leq 3.0$		

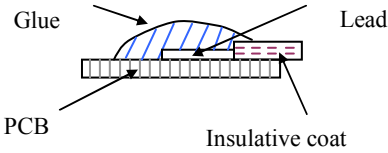




			B	Glass crack should not cover patterns u and alignment mark and patterns.	
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Inspection items		Judgment standard	
		Category(application: B zone)	
16	PCB defect	<p>Component soldering: No cold soldering、short、open circuit、burr、tin ball The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1); the sheet component deviation: Pin deviates from the pad and contact with the near components is not permitted (Pic.2)</p>	
		<p>lead defect: The lead lack must be less than 1/3 of its width; The lead burr must be less than 1/3 of the seam; Impurities connect with the near leads is not permitted</p>	
		<p>Connector soldering: Soldering tin is at contact position of the plug and socket is not permitted No foundation is scald Serious cave distortion on plug and socket contact pin is not permitted</p>	



	<p>Glue on root of the speaker receiver and motor lead: The insulative coat of the lead must join into the PCB; the protected glue must envelop to the insulative coat.</p>	
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10. Precautions for Use of LCD Modules

10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.



10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- a. Be sure to ground the body when handling the LCD Modules.
- b. Tools required for assembly, such as soldering irons, must be properly ground.
- c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range.

If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity: $\leq 80\%$

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.



10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

